



ISOLATED FEEDBACK CIRCUIT

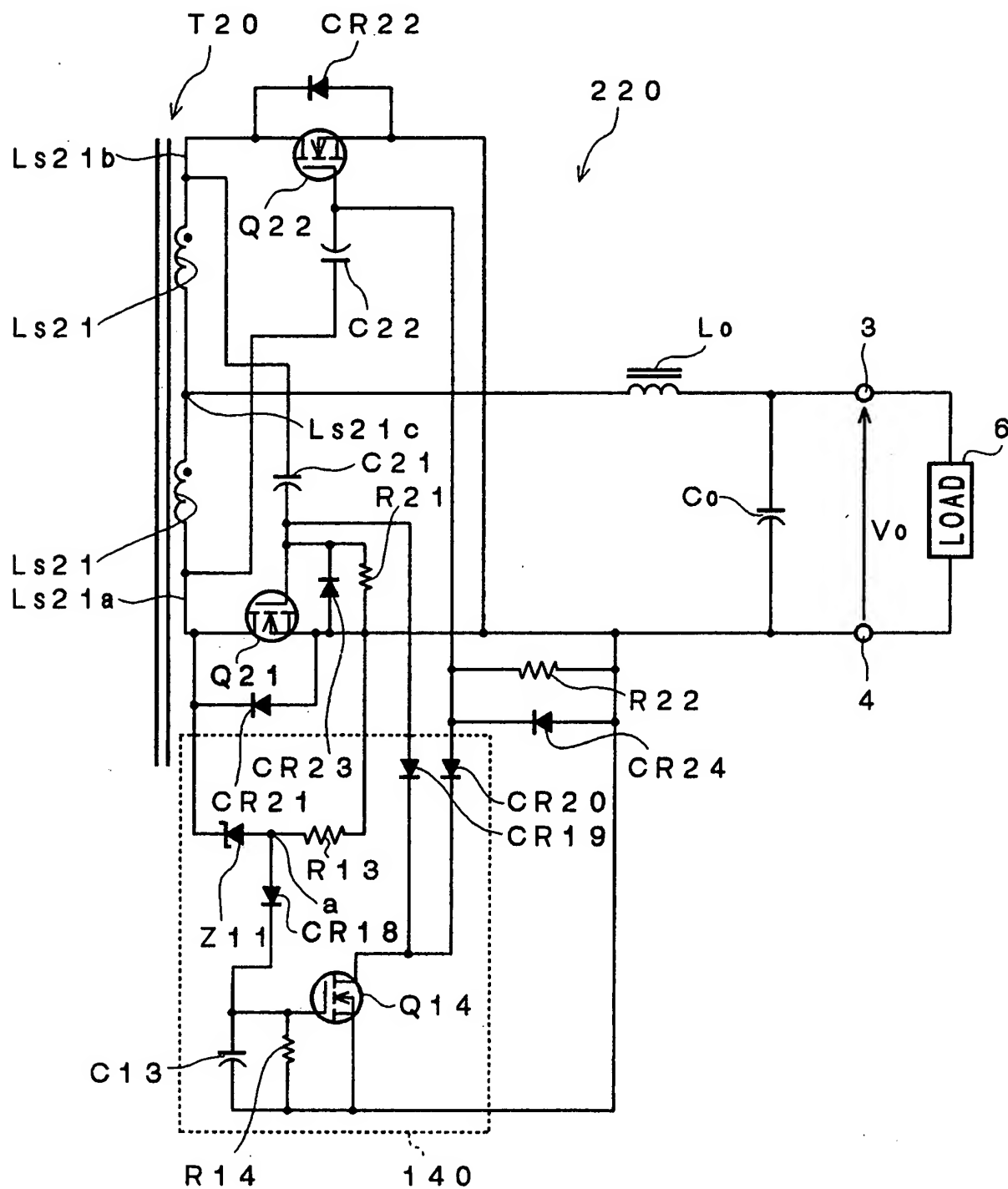


FIG. 2

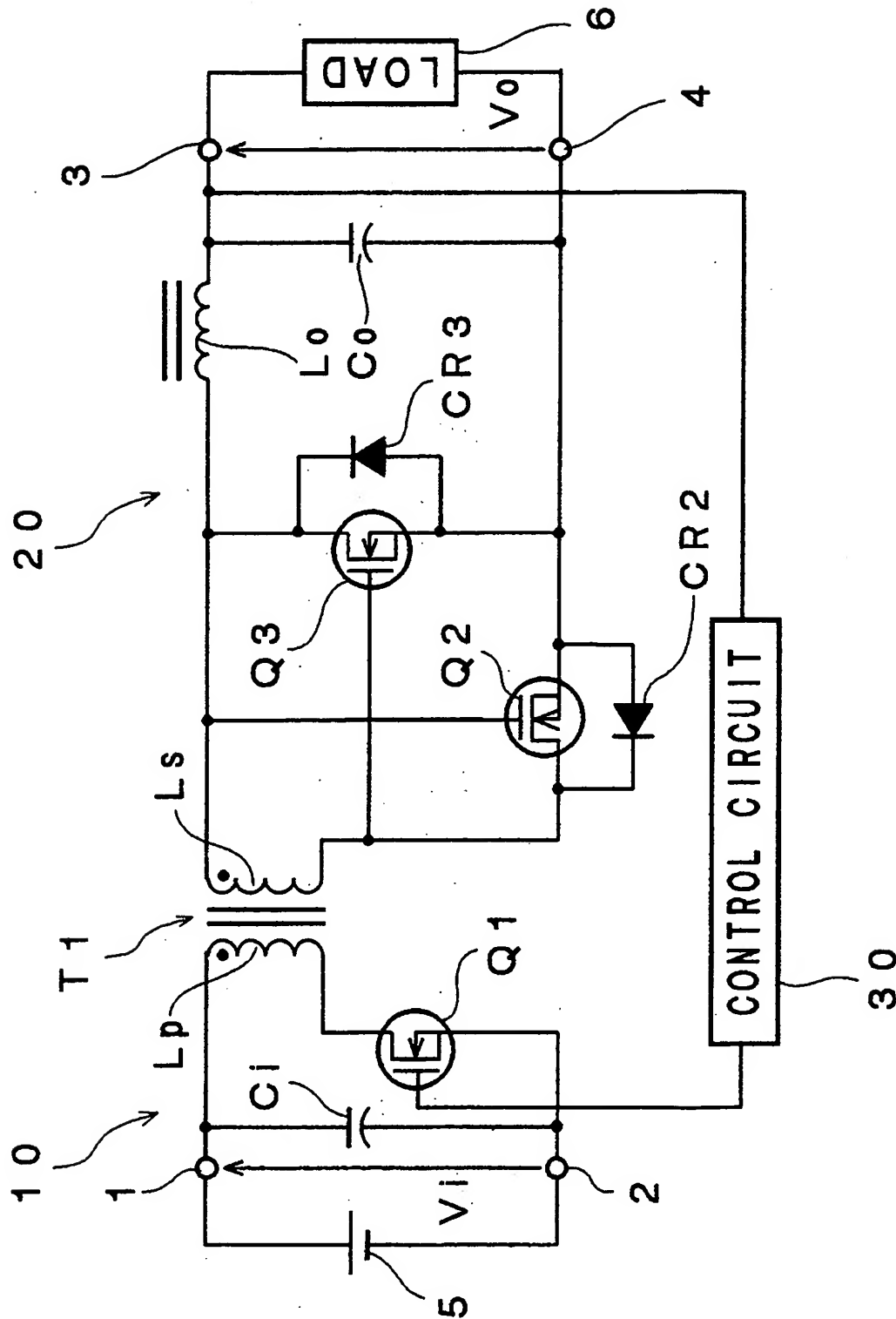


FIG. 3
 (Prior Art)

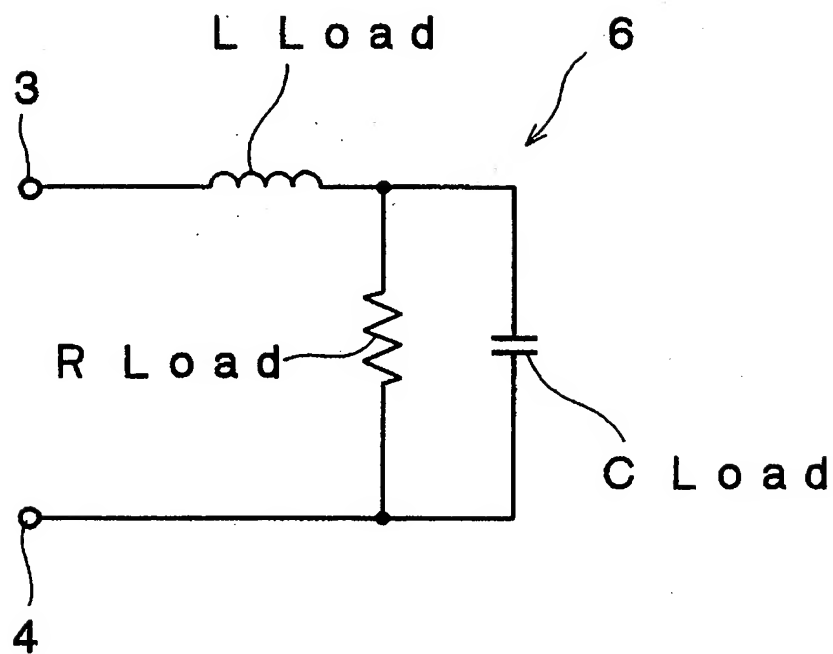


FIG. 4
(Prior Art)

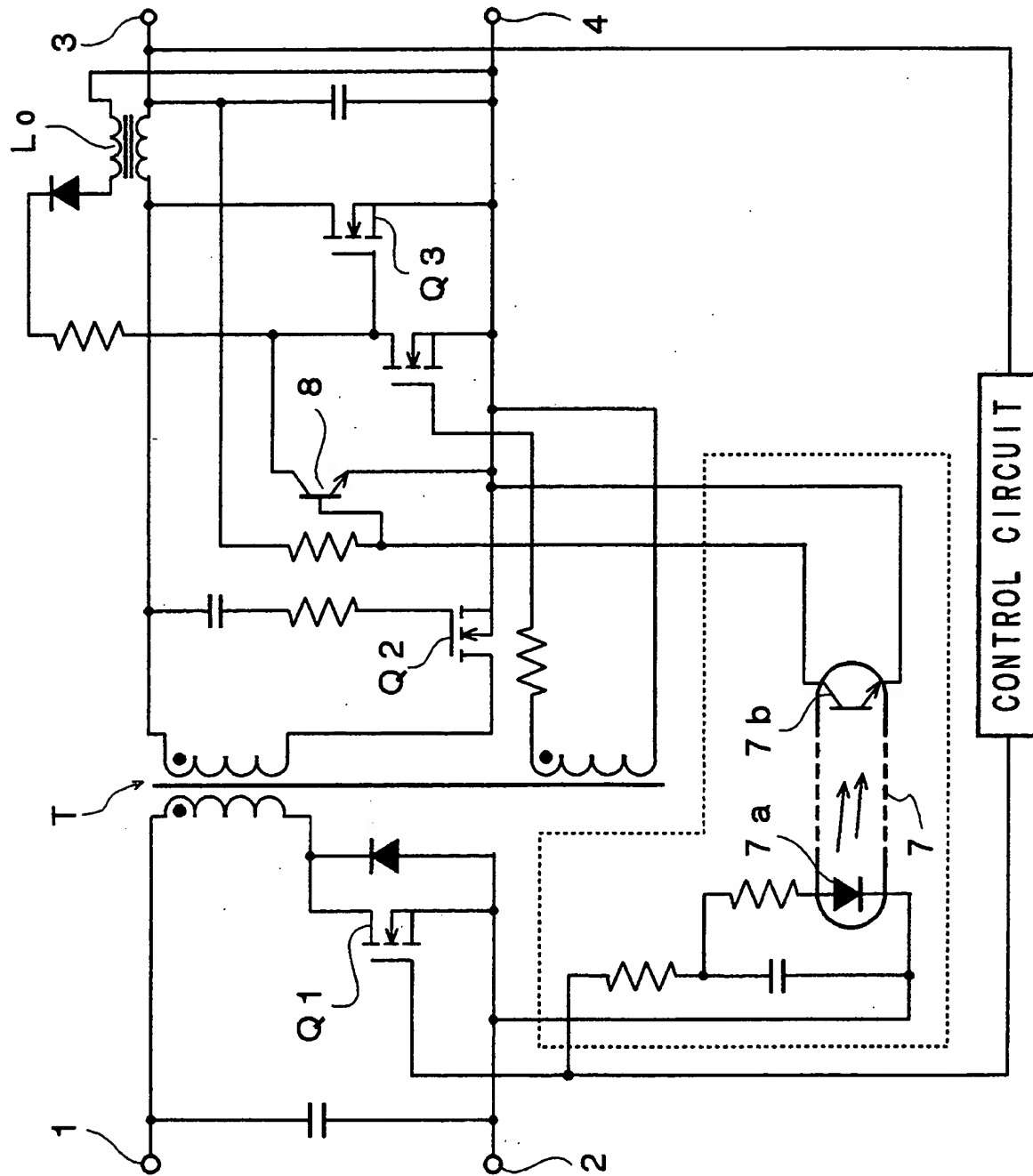


FIG. 5
(Prior Art)

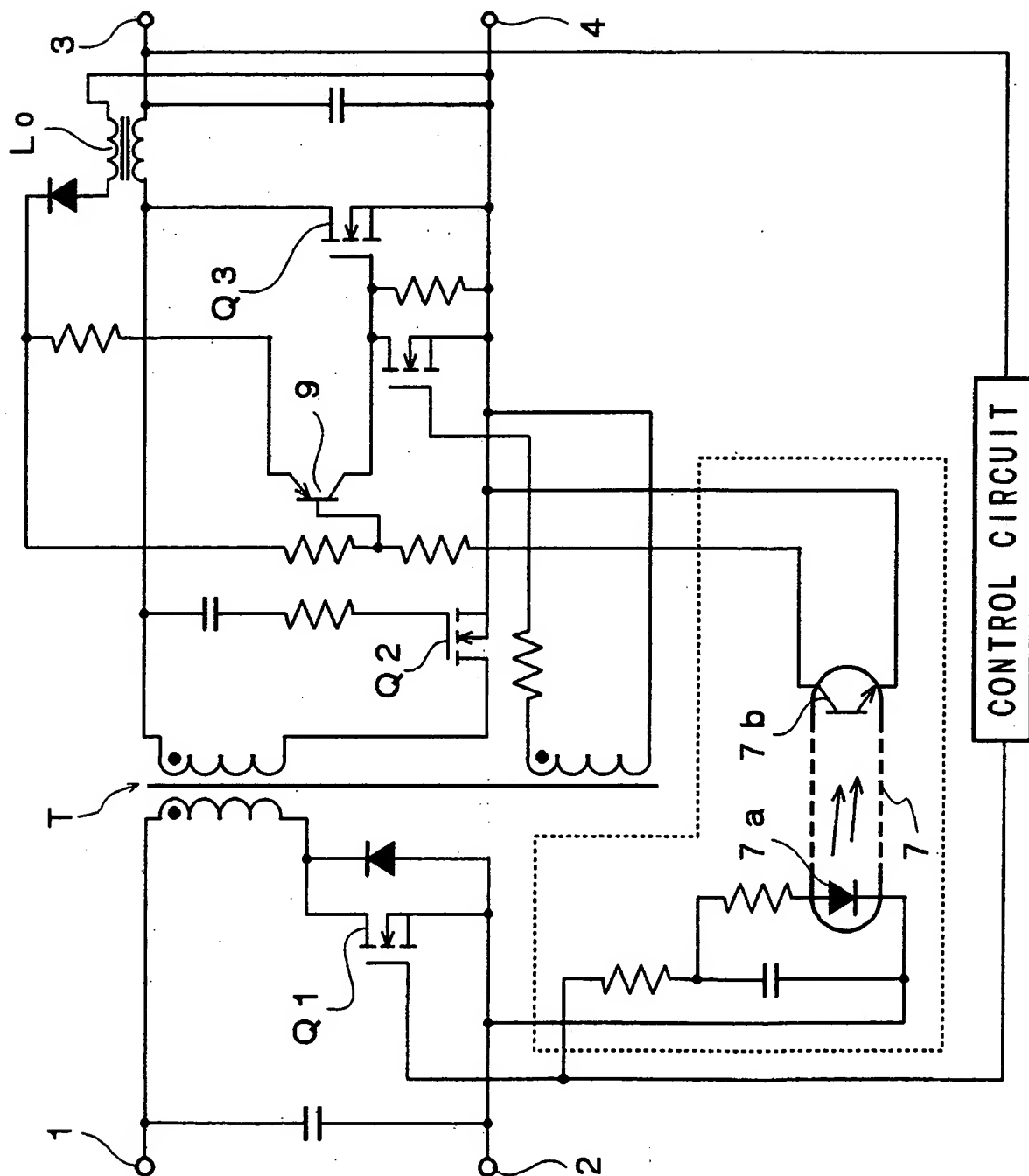


FIG. 6
(Prior Art)